BOG 10: OS/R Node Local

ASCR Workshop on Extreme Heterogeneity in HPC 23-25 Jan 2018

Moderator(s): Michael Lang

BOGS X Contributors

- \rightarrow Barry Rountree
- Beard, Jonathan \rightarrow
- \rightarrow Brian Kocoloski
- \rightarrow Chris Rossbach
- \rightarrow Christian Engelmann
- David Bernholdt \rightarrow
- Gokhale, Maya B. \rightarrow
- Kenneth Moreland \rightarrow
- \rightarrow Lang, Mike
- \rightarrow Larry Kaplan
- \rightarrow Lofstead, Gerald F II
- \rightarrow Mattan
- \rightarrow Noah Watkins
- \rightarrow Nowell, Lucy
- Paul Peltz Jr \rightarrow
- Ron Brightwell \rightarrow
- sriram krishnamoorthy \rightarrow
- Suren Byna \rightarrow
- \rightarrow Sweeney, Christine
- \rightarrow Terry Jones
- \rightarrow Vetter, Jeffrey S.
- Yoonho Park \rightarrow
- \rightarrow Doug Jacobsen
- \rightarrow Tina Declerck
- Rebecca Hartman-Baker \rightarrow
- Latchesar lonkov \rightarrow
- \rightarrow Vitus Leung
- \rightarrow Pete Beckman\
- \rightarrow Kamil Iskra
- \rightarrow Andrew J. Younge
- \rightarrow Brian Van Essen

rountree@llnl.gov

ionathan.beard@arm.com

brian.kocoloski@wustl.edu

rossbach@cs.utexas.edu

engelmannc@computer.org

bernholdtde@ornl.gov

gokhale2@llnl.gov

kmorel@sandia.gov

mlang@lanl.gov

lkaplan@cray.com

gflofst@sandia.gov

mattan.erez@utexas.edu

jayhawk@cs.ucsc.edu

Lucy.Nowell@science.doe.gov

peltz@lanl.gov

rbbrigh@sandia.gov

sriram.krishnamoorthy@gmail.com

sbyna@lbl.gov

cahrens@lanl.gov

triones@ornl.gov

vetter@computer.org

yoonho@us.ibm.com

dmiacobsen@lbl.gov

tmdeclerck@lbl.gov

rjhartmanbaker@lbl.gov

lionkov@lanl.gov

BOG 10 OS/R Challenge Assessment

Discussion to identify research required to get from where the capabilities are now to where to where they need to be by 2030.

Memory - storage is blurring

Definition of a Node/Rack is blurring

Many distributed processing elements of differing characteristics (power, sequential, parallel, specialized, reconfigurable, BW, Latency...)

Mostly Static Resources vs Dynamic (FPGA can have 2 cores, then 10 cores)

Trapped between two ill defined areas - Hardware and Programming models

BOG 10 OS/R Targets for 2030 (**PRDs)

- 1. OS-R / Hardware co-design for unified heterogeneous components: computational, active memory, network, and storage components
 - a. Virtual memory system redesign
 - b. Scheduling/discovery/composition/arbitration across dynamic heterogeneous components
 - c. Non-Linux OS alternatives, non POSIX, rearchitecting control plane / data plane boundaries
 - d. Virtualization and containerization
- 2. Decentralized Resource Management
 - a. Interfaces between multiple local OSes; interfaces between LOS and GOS
- 3. Applying ML/AI to OS and runtime resource management
 - a. Leveraging accelerators for OS/R needs including global OS
 - b. autonomy

BOG 10 Capability Targets for Extreme Heterogeneity

BOG X brainstorming and discussion of capabilities that will be needed in the 2025-2035 timeframe to make increasingly heterogeneous hardware technologies useful and productive for science applications.

BOG 10 OS/R

What does an OS do -- local resource manager:

Processing, Memory, Communication, Storage

What is changing:

Processing, Memory, Communication, Storage

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- 2. Non-Linux OS alternatives, non POSIX, rearchitecting control plane / data plane boundaries
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- Virtualization and containerization
- 5. Applying ML/AI to OS and runtime resource management
 - a. Leveraging accelerators for OS needs

BOG 10 OS/R Current Status

BOG X survey describing the current status of science, technology, or practice related to this theme (starting with material directly from the FSD).

BOG 10 OS/R Current Capability

Capability 1: Basic support for accelerators

Fine-grained support of CPU, memory

Coarse-grained support of Accelerators

Capability 2: limited interaction/communication between LOS and GOS

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BOG 10 OS/R: list of key research challenges

Challenge X.1

Challenge X.2

BOG 10 OS/R Possible Research Directions Summary

PRD X.1 - direction 1

PRD X.2 - direction 2

PRD X.n - direction n

PRD X.n: Short title of possible research direction

- One paragraph description (3 sentence/bullet)
- Research challenges
 - Metrics for progress
- Potential research approaches and research directions
- How and when will success impact technology?